

ABSTRACT

In an SDH transmission apparatus, an intra-apparatus reference frame timing produced by a main signal processing unit (3A/3B) which accommodates a plurality of interface units (2) is distributed to the interface units (2) by a frame timing production section (32-9) to suppress displacements of the frame top positions of the main signals from the IF boards to the minimum, whereby frame timing re-clocking of the main signals is performed with the least necessary memory capacity without using a pointer processing technique so that, even if the number of channels to be processed increases, the apparatus scale can be suppressed to the minimum.

1000759-103101